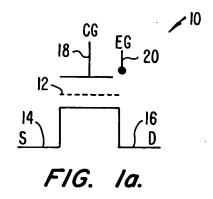
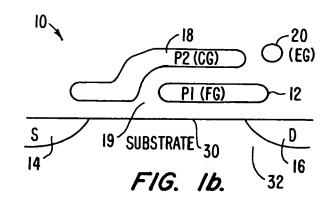
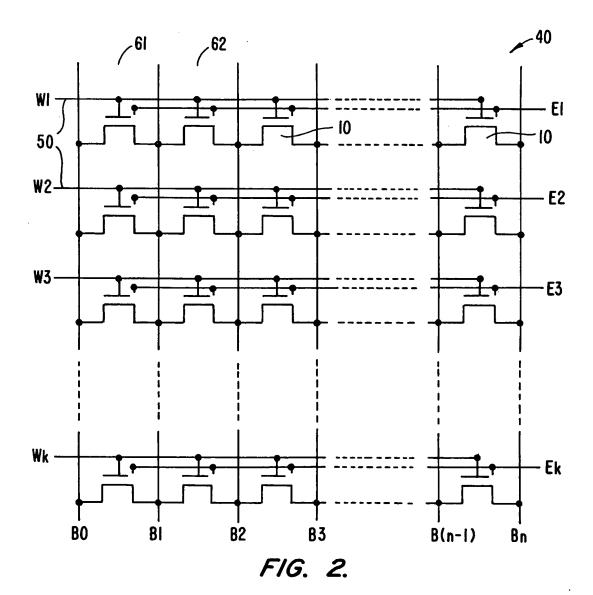
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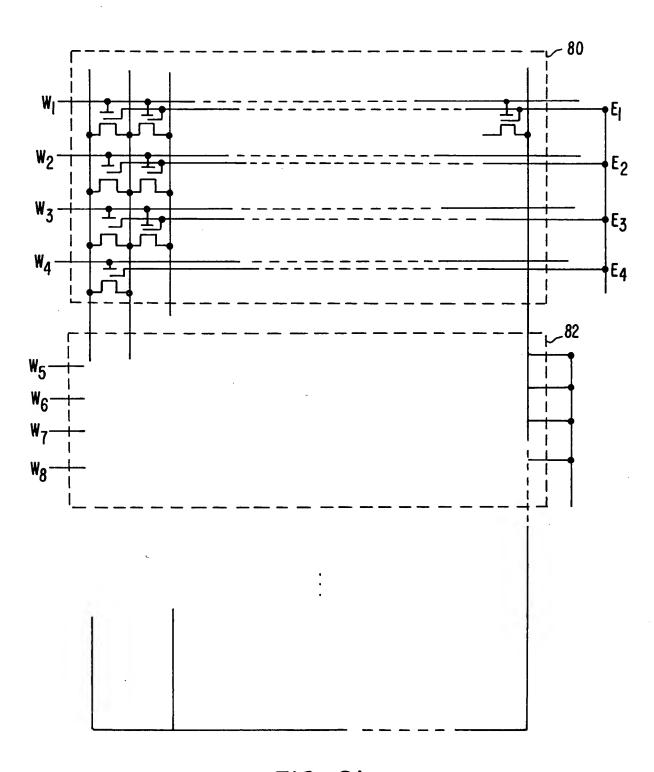


FIG. 2b.

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VOLTAGE CONDITIONS FOR NORMAL MEMORY OPERATIONS

TYPE OF LINES	READ	PROGRAM	ERASE
SELECTED WORD LINE (WL) NON-SELECTED WORD LINE (WL)	5 V 0 V	12V 0V	ov ov
SELECTED SOURCE LINE (SL)	OV	OV	OV
SELECTED DRAIN LINE (DL) NON-SELECTED BIT LINES (BL)	1.5V Float	8V FLOAT	0V 0V
SELECTED ERASE LINE (EL)	OV	OV	20V
NON-SELECTED ERASE LINE (EL)	OV	OV	0 V

FIG. 3.

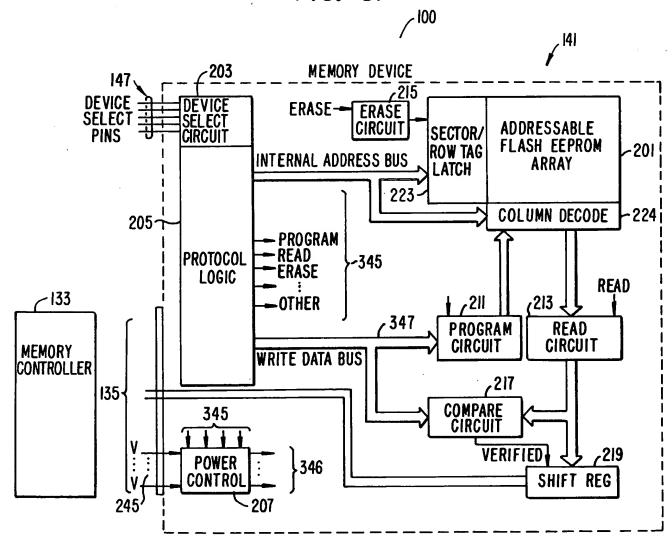
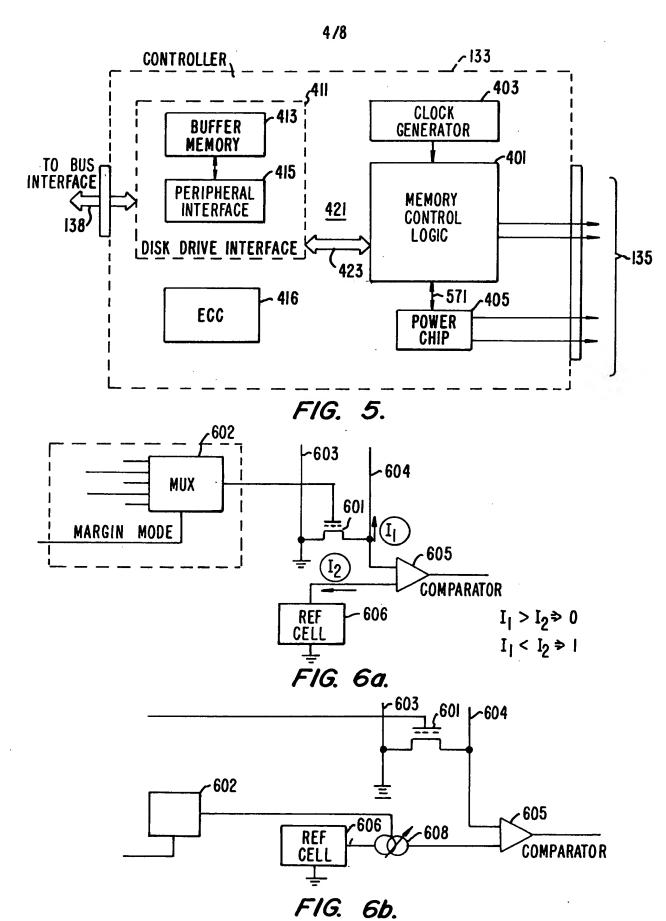
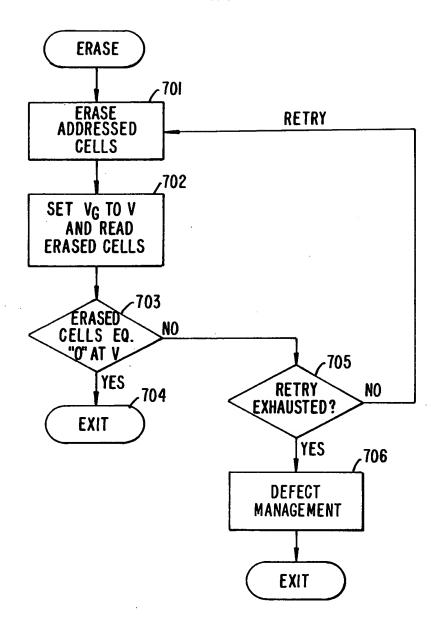


FIG. 4.

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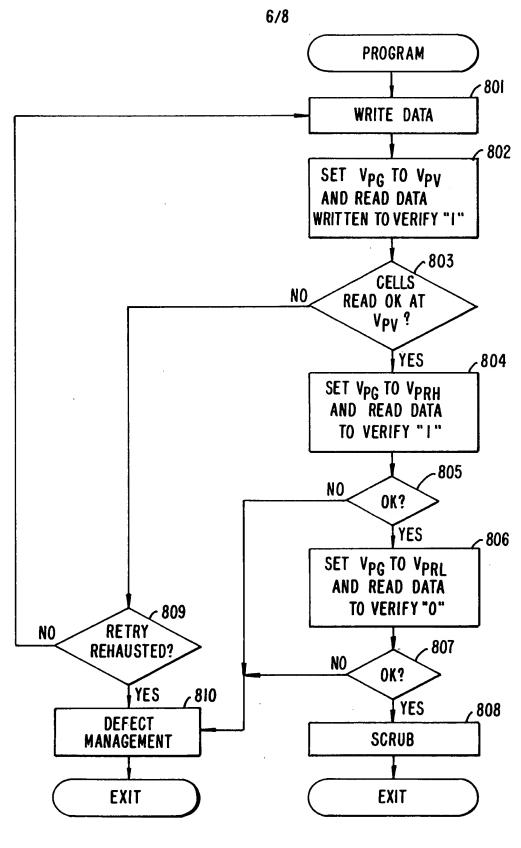


FIG. 8.

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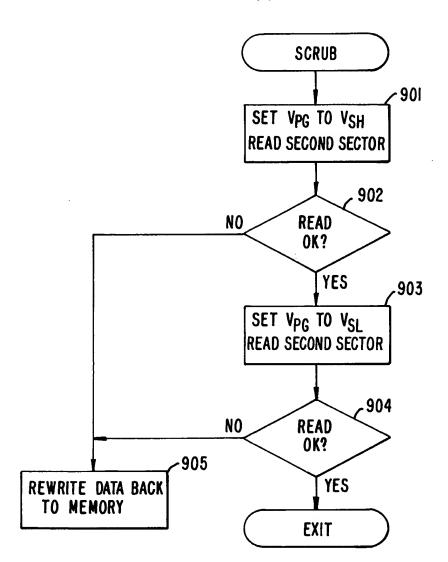


FIG. 9.

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